

Notice of References Cited	Application/Control No. 10/619,961	Applicant(s)/Patent Under Reexamination AHMED ET AL.	
	Examiner Richard A. Booth	Art Unit 2812	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,350,696	02-2002	Shields et al.	438/704
	B	US-5,032,530	07-1991	Lowrey et al.	438/231
	C	US-5,424,234	06-1995	Kwon, Ho Y.	438/305
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., "Silicon Processing for the VLSI Era Volume 1: Process Integration, pages 183-194, 1986, Lattice Press.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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